NOTES: (UNLESS OTHERWISE SPECIFIED)

1 INTERPRET DIMENSIONS AND TOLERANCES PER ANSI Y14.5M-2002.

2 ALL GEOMETRY IS TO BE DERIVED FROM THE CAD DATABASE. CRITICAL TO FUNCTION DIMENSIONS CALLED OUT ON THIS DRAWING ARE ONLY FOR INSPECTION PURPOSES.

5 MOLD FINISH TO BE "SPI C-2" ON ALL INTERIOR (NON-APPEARANCE) UNSPECIFIED SURFACES.

[6] COSMETIC SURFACES TO BE "SPI D-2" WHERE INDICATED. ALL SUB-SURFACE SURFACES OF NUMBER TO BE "SPI A-2"

[7] COSMETIC SURFACES TO BE "SPI D-2" WHERE INDICATED.

8 PARTING LINE MISMATCH AND FLASH NOT TO EXCEED 0.05.

9 MOLD DESIGN TO MINIMIZE GATE BLUSH, FLOW LINES AND WELD MARKS. MOLD CONSTRUCTION TO CONFORM TO GOOD MOLDING INDUSTRY PRACTICES AS STATED IN THE CURRENT EDITION OF "STANDARDS AND PRACTICES OF PLASTIC CUSTOM MOLDERS BY THE SOCIETY OF THE PLASTICS INDUSTRY, INC.

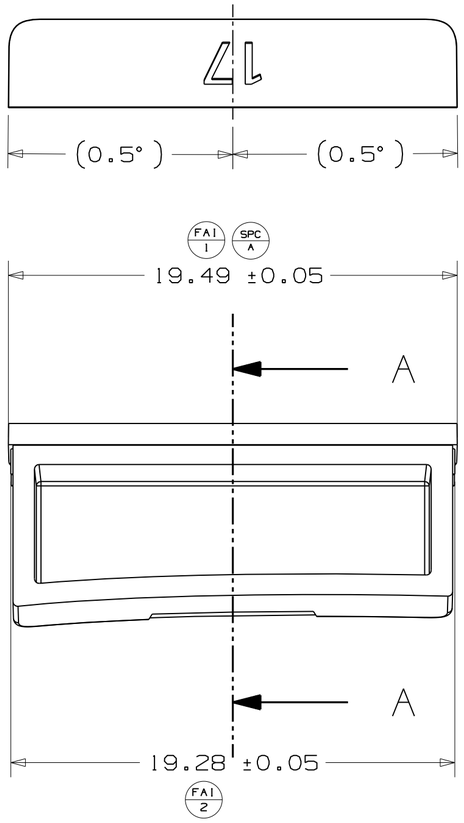
12 GATE VESTIGE OR TRIM TO BE SUB FLUSH.

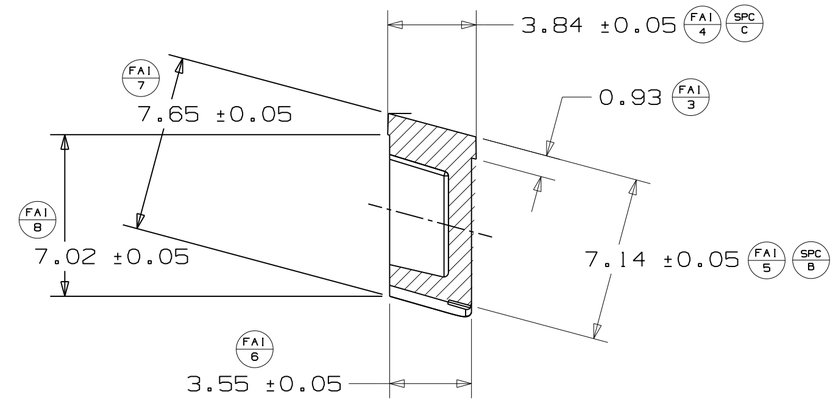
13 0% REGRIND PERMISSIBLE.

14 PART TO BE FREE OF MOLD RELEASE ON APPEARANCE SURFACES.

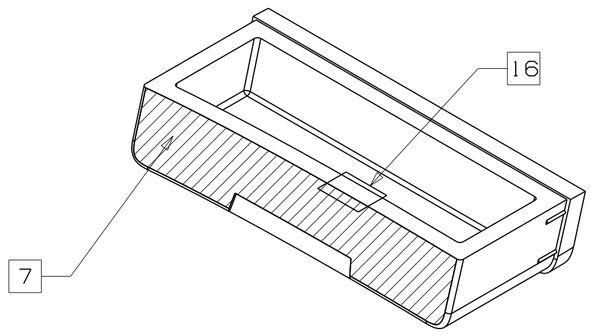
15 TOLERANCES DESIGNATED WITH SYMBOL (SPC XXX) SHALL BE PRODUCED WITH STATISTICAL TOLERANCE PROCESS CONTROLS.

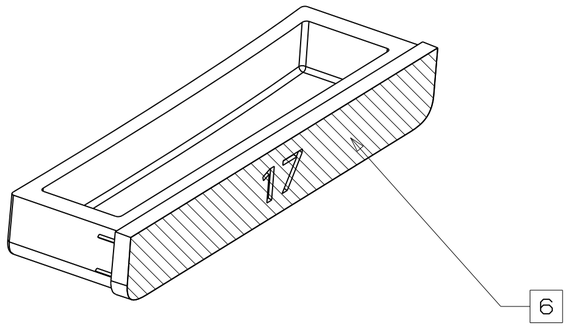
[16] TOOL AND CAVITY # NOTED AT LOCATION INDICATED.





SECTION A - A





METRIC		Apple Computer, Inc.
DRAFTER: M YU	DATE: 03/14/08	NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING: (i) TO MAINTAIN THIS DOCUMENT IN CONFIDENCE (ii) NOT TO REPRODUCE OR COPY IT (iii) NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART (iv) ALL RIGHTS RESERVED
DESIGNER: IPOD PD	DATE: 03/14/08	
DIMENSIONS ARE IN MILLIMETERS TOLERANCES 0 > - < 2 ±0.05 2 > - < 10 ±0.08 10 > - < 50 ±0.10 50 > - < 100 ±0.15 100 > - < 200 ±0.20 200 > - ±0.25 ANGLES ±0.50° DO NOT SCALE DRAWINGS		TITLE: iPod nano (4th Generation)
THIRD ANGLE PROJECTION	SIZE: C	DRAWING NUMBER: 818-0156 REV: A
		SCALE: 5:1 SHT 1 OF 1

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