## **Dual General Purpose Transistor**

The MBT3906DW1T1G device is a spin-off of our popular SOT-23/SOT-323 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-363 six-leaded surface mount package. By putting two discrete devices in one package, this device is ideal for low-power surface mount applications where board space is at a premium.

### **Features**

 $h_{FF}$ , 100–300

Low  $V_{CE(sat)}$ ,  $\leq 0.4 \text{ V}$ 

Simplifies Circuit Design

Reduces Board Space

Reduces Component Count

Available in 8 mm, 7-inch/3,000 Unit Tape and Reel

These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V <sub>CEO</sub>	-40	Vdc
Collector - Base Voltage	V <sub>CBO</sub>	-40	Vdc
Emitter – Base Voltage	V <sub>EBO</sub>	-5.0	Vdc
Collector Current - Continuous	Ic	-200	mAdc
Electrostatic Discharge	ESD	HBM Class 2 MM Class B	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### THERMAL CHARACTERISTICS

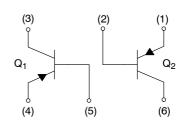
Characteristic	Symbol	Max	Unit
Total Package Dissipation (Note 1) T <sub>A</sub> = 25 C	P <sub>D</sub>	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	833	C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	С

Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.



## ON Semiconductor®

http://onsemi.com





SOT-363/SC-88 CASE 419B STYLE 1

## **MARKING DIAGRAM**



A2 = Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MBT3906DW1T1G	SOT-363 (Pb-Free)	3000 / Tape & Reel

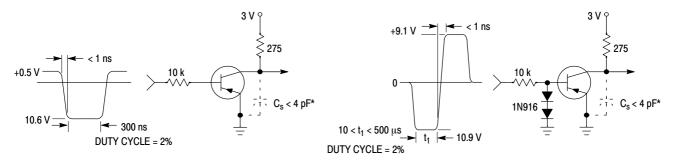
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25 C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit	
OFF CHARACTI	ERISTICS					
Collector – Emitter	Breakdown Voltage (Note 2)	V <sub>(BR)CEO</sub>	-40	_	Vdc	
Collector - Base B	reakdown Voltage	V <sub>(BR)CBO</sub>	-40	-	Vdc	
Emitter – Base Breakdown Voltage		V <sub>(BR)EBO</sub>	-5.0	-	Vdc	
Base Cutoff Curre	nt	I <sub>BL</sub>	-	-50	nAdc	
Collector Cutoff C	urrent	I <sub>CEX</sub>	_	-50	nAdc	
ON CHARACTE	RISTICS (Note 2)	<u>.</u>			•	
$(I_C = -1.0 \text{ mAdc})$ $(I_C = -10 \text{ mAdc})$ $(I_C = -50 \text{ mAdc})$	c, $V_{CE} = -1.0 \text{ Vdc}$ ) c, $V_{CE} = -1.0 \text{ Vdc}$ ) , $V_{CE} = -1.0 \text{ Vdc}$ ) , $V_{CE} = -1.0 \text{ Vdc}$ ) c, $V_{CE} = -1.0 \text{ Vdc}$ )	h <sub>FE</sub>	60 80 100 60 30	- - 300 - -	-	
$(I_C = -10 \text{ mAdc})$	Saturation Voltage , $I_B = -1.0 \text{ mAdc}$ , $I_B = -5.0 \text{ mAdc}$	V <sub>CE(sat)</sub>	- -	-0.25 -0.4	Vdc	
	turation Voltage , $I_B = -1.0 \text{ mAdc}$ ) , $I_B = -5.0 \text{ mAdc}$ )	V <sub>BE(sat)</sub>	-0.65 -	-0.85 -0.95	Vdc	
SMALL-SIGNA	L CHARACTERISTICS					
Current - Gain - B	andwidth Product	f <sub>T</sub>	250	-	MHz	
Output Capacitan	ce	C <sub>obo</sub>	-	4.5	pF	
Input Capacitance	)	C <sub>ibo</sub>	-	10.0	pF	
Input Impedance (V <sub>CE</sub> = -10 Vdc	r, I <sub>C</sub> = -1.0 mAdc, f = 1.0 kHz)	h <sub>ie</sub>	2.0	12	kΩ	
Voltage Feedback (V <sub>CE</sub> = -10 Vdc	Ratio , I <sub>C</sub> = -1.0 mAdc, f = 1.0 kHz)	h <sub>re</sub>	0.1	10	X 10 <sup>-4</sup>	
Small – Signal Cur (V <sub>CE</sub> = -10 Vdc	rent Gain , I <sub>C</sub> = –1.0 mAdc, f = 1.0 kHz)	h <sub>fe</sub>	100	400	-	
Output Admittance (V <sub>CE</sub> = -10 Vdc	e , I <sub>C</sub> = -1.0 mAdc, f = 1.0 kHz)	h <sub>oe</sub>	h <sub>oe</sub> 3.0 60		μmhos	
Noise Figure (V <sub>CE</sub> = -5.0 Vde	c, $I_C = -100 \mu Adc$ , $R_S = 1.0 k \Omega$ , $f = 1.0 kHz$ )	NF	-	4.0	dB	
SWITCHING CH	ARACTERISTICS					
Delay Time	$(V_{CC} = -3.0 \text{ Vdc}, V_{BE} = 0.5 \text{ Vdc})$	t <sub>d</sub>	_	35	ns	
Rise Time	$(I_C = -10 \text{ mAdc}, I_{B1} = -1.0 \text{ mAdc})$	t <sub>r</sub>	-	35		
Storage Time	$(V_{CC} = -3.0 \text{ Vdc}, I_{C} = -10 \text{ mAdc})$	$(V_{CC} = -3.0 \text{ Vdc}, I_C = -10 \text{ mAdc})$ $t_s$ - 225		225		
Fall Time	$(I_{B1} = I_{B2} = -1.0 \text{ mAdc})$	t <sub>f</sub>	_	75	ns	

Fall Time  $(I_{B1} = I_{B2} = -1.0 \text{ mAdc})$ 2. Pulse Test: Pulse Width  $\leq$  300  $\mu$ s; Duty Cycle  $\leq$  2.0%.



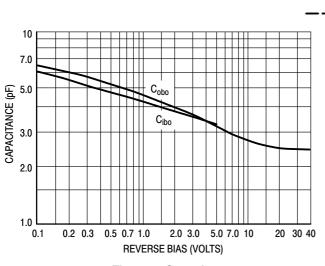
\* Total shunt capacitance of test jig and connectors

Figure 1. Delay and Rise Time Equivalent Test Circuit

Figure 2. Storage and Fall Time Equivalent Test Circuit

## TYPICAL TRANSIENT CHARACTERISTICS

- T<sub>J</sub> = 25 C



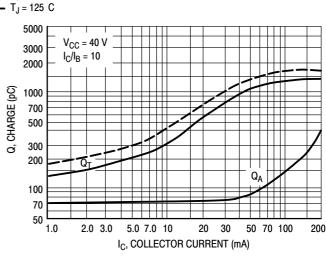
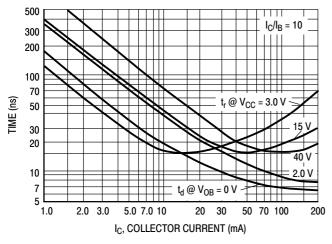


Figure 3. Capacitance

Figure 4. Charge Data



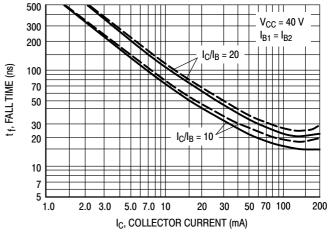
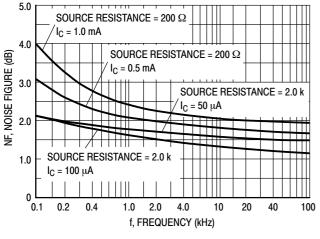


Figure 5. Turn - On Time

Figure 6. Fall Time

# TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

 $(V_{CE} = -5.0 \text{ Vdc}, T_A = 25 \text{ C}, Bandwidth} = 1.0 \text{ Hz})$ 



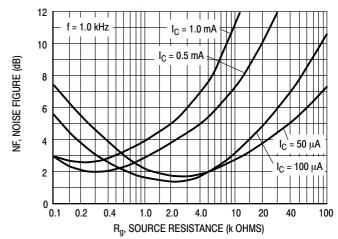
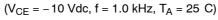
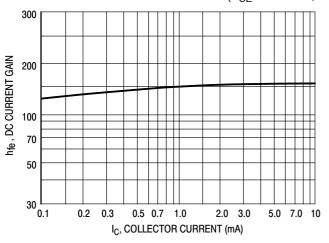


Figure 7.

Figure 8.

## **h PARAMETERS**





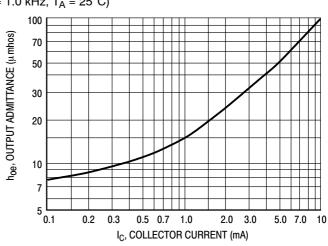
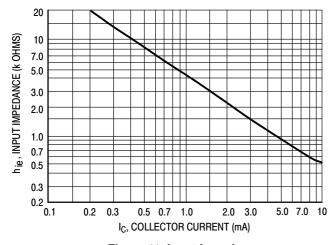


Figure 9. Current Gain

Figure 10. Output Admittance



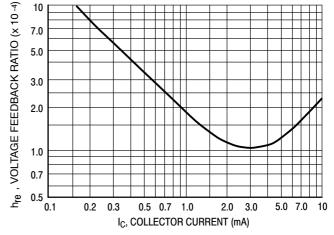


Figure 11. Input Impedance

Figure 12. Voltage Feedback Ratio

## TYPICAL STATIC CHARACTERISTICS

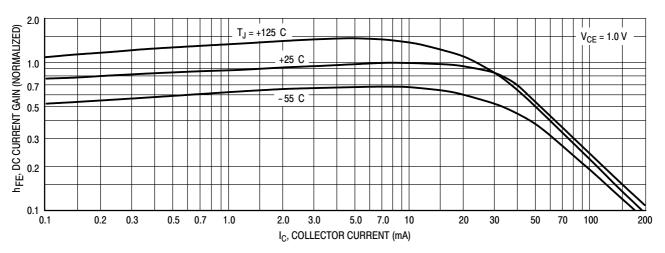


Figure 13. DC Current Gain

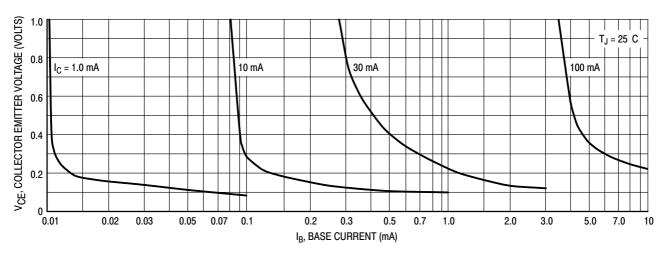


Figure 14. Collector Saturation Region

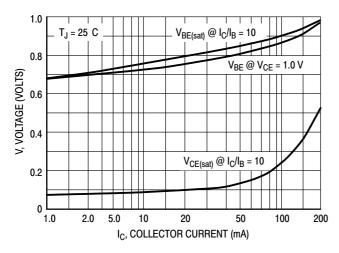


Figure 15. "ON" Voltages

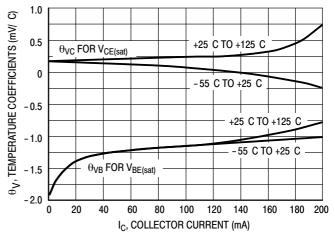
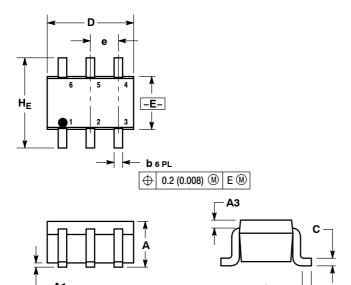


Figure 16. Temperature Coefficients

### PACKAGE DIMENSIONS

SC-88/SC70-6/SOT-363 CASE 419B-02 **ISSUE W** 



#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI
- 2 CONTROLLING DIMENSION: INCH
- 3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

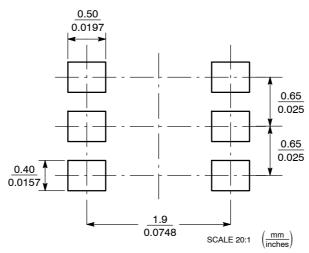
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.95	1.10	0.031	0.037	0.043
A1	0.00	0.05	0.10	0.000	0.002	0.004
А3	0.20 REF			0.008 REF		
b	0.10	0.21	0.30	0.004	0.008	0.012
С	0.10	0.14	0.25	0.004	0.005	0.010
D	1.80	2.00	2.20	0.070	0.078	0.086
Е	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65 BSC		0.026 BSC			
L	0.10	0.20	0.30	0.004	0.008	0.012
He	2 00	2 10	2 20	0.078	0.082	0.086

### STYLE 1:

PIN 1. EMITTER 2

- 2. BASE 2
- 3. COLLECTOR 1
- 4. EMITTER 1
- 5. BASE 1 6. COLLECTOR 2

## **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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