



DST857BDJ

DUAL PNP SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Planar Die Construction
- Ideally Suited for Automated Assembly Processes
- Complementary NPN Type Available (DST847BDJ)
- Lead, Halogen and Antimony Free, RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Ultra Small Package

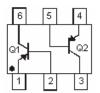
Mechanical Data

- Case: SOT-963
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208
- Weight: 0.0027 grams (approximate)

SOT-963



Top View



Device Schematic

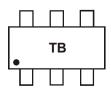
Ordering Information

Ī	Device	Packaging	Shipping	
	DST857BDJ-7	SOT-963	10,000/Tape & Reel	

Notes:

- 1. No purposefully added lead. Halogen and Antimony Free.
- 2. Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com

Marking Information



TB = Product Type Marking Code



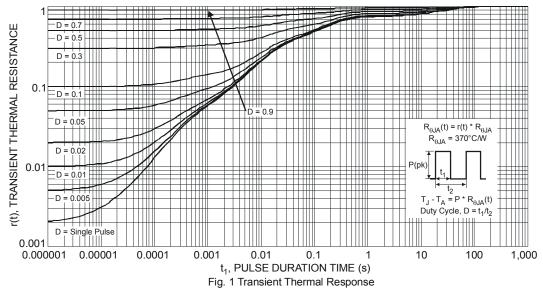
Maximum Ratings @TA = 25°C unless otherwise specified

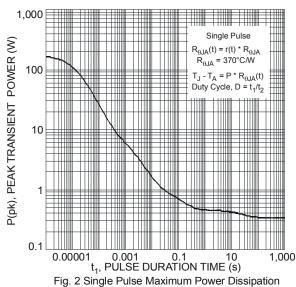
Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-50	V
Collector-Emitter Voltage	V _{CEO}	-45	V
Emitter-Base Voltage	V _{EBO}	-5.0	V
Collector Current - Continuous (Note 3)	I _C	-100	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	P _D	300	mW
Thermal Resistance, Junction to Ambient (Note 3)	$R_{ heta JA}$	417	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes: 3. Device mounted on FR-4 PCB with minimum recommended pad layout.





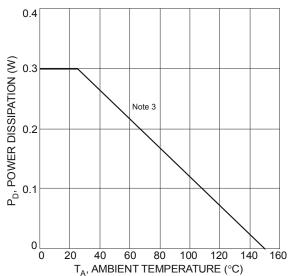


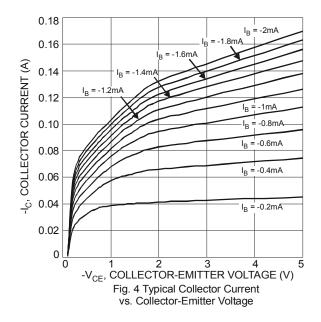
Fig. 3 Power Dissipation vs. Ambient Temperature

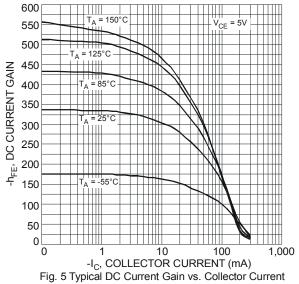


Electrical Characteristics @T_A = 25°C unless otherwise specified

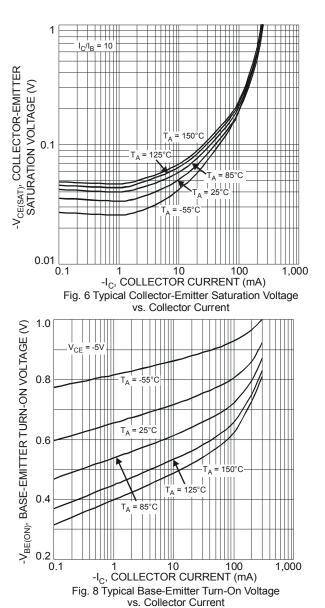
Characteristic (Note 4)	Symbol	Min	Typical	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-50	-100	-	V	$I_C = -10\mu A$, $I_B = 0$
Collector-Emitter Breakdown Voltage	V _{(BR)CES}	-50	-90	-	V	$I_C = -10\mu A$, $I_B = 0$
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-45	-65	-	V	$I_{C} = -1 \text{mA}, I_{B} = 0$
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-6	-8.5	-	V	$I_E = -1 \mu A$, $I_C = 0$
Collector Cutoff Current	I _{CBO}	-	-	-15	nA	V _{CB} = -30V
DC Current Gain	h _{FE}	100 200	340 330	- 470	-	$I_C = -10\mu A$, $V_{CE} = -5V$ $I_C = -2.0mA$, $V_{CE} = -5V$
Collector-Emitter Saturation Voltage	V _{CE(sat)}	-	-70 -300	-175 -500	mV	$I_C = -10$ mA, $I_B = -0.5$ mA $I_C = -100$ mA, $I_B = -5.0$ mA
Base-Emitter Saturation Voltage	V _{BE(sat)}	-	-760 -885	-1000 -1100	mV	I_C = -10mA, I_B = -0.5mA I_C = -100mA, I_B = -5.0mA
Base-Emitter Voltage	V _{BE(on)}	-600	-670 -715	-780 -850	mV	I_C = -2.0mA, V_{CE} = -5V I_C = -10mA, V_{CE} = -5V
Current Gain-Bandwidth Product	f _T	100	340	-	MHz	$V_{CE} = -5V, I_{C} = -10mA,$ f = 100MHz
Output Capacitance	C _{obo}	-	2.0	-	pF	V _{CB} = -10V, f = 1.0MHz

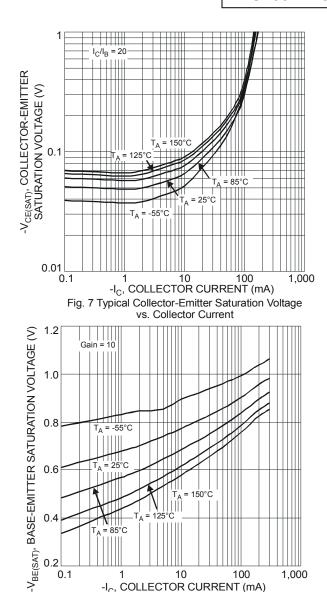
Notes: 4. Short duration pulse test used to minimize self-heating effect.











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-I_C, COLLECTOR CURRENT (mA)

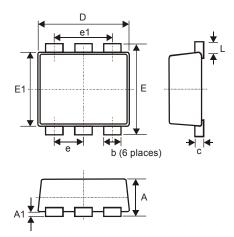
Fig. 9 Typical Base-Emitter Saturation Voltage

vs. Collector Current

100

1,000

Package Outline Dimensions

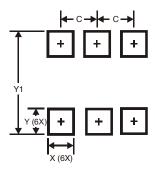


SOT-963					
Dim	Min	Max	Тур		
Α	0.40	0.50	0.45		
A1	0	0.05	-		
С	0.120	0.180	0.150		
D	0.95	1.05	1.00		
E	0.95	1.05	1.00		
E1	0.75	0.85	0.80		
L	0.05	0.15	0.10		
b	0.10	0.20	0.15		
е	0.35 Typ				
e1	0.70 Typ				
All Dimensions in mm					

0.1



Suggest Pad Layout



Dimensions	Value (in mm)
С	0.350
Х	0.200
Υ	0.200
Y1	1.100

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